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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/766,270	01/29/2004	Rajendran Nair		3444
7	7590 03/16/2006 .		EXAMINER	
Rajendran Nair, ComLSI Inc.			BERHANE, ADOLF D	
3838 E. Encinas Ave. Gilbert, AZ 85234			ART UNIT	PAPER NUMBER
•			2838	
			DATE MAILED: 03/16/2006 .	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)
	10/766,270	NAIR, RAJENDRAN
Office Action Summary	Examiner	Art Unit
	Adolf Berhane	2838
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet w	rith the correspondence address
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period was pailing to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUN 36(a). In no event, however, may a vill apply and will expire SIX (6) MO , cause the application to become A	CATION. reply be timely filed NTHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).
Status		
3) Since this application is in condition for allowar closed in accordance with the practice under E Disposition of Claims 4) Claim(s) 1-20 is/are pending in the application. 4a) Of the above claim(s) is/are withdraw 5) Claim(s) is/are allowed. 6) Claim(s) 1-20 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or	action is non-final. nce except for formal ma ix parte Quayle, 1935 C.I wn from consideration.	
Application Papers		
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) accomposed and accomposed accompos	epted or b) objected to drawing(s) be held in abeya tion is required if the drawin	nce. See 37 CFR 1.85(a). g(s) is objected to. See 37 CFR 1.121(d).
Priority under 35 U.S.C. § 119		
 12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority document: 2. Certified copies of the priority document: 3. Copies of the certified copies of the priority document: application from the International Bureau * See the attached detailed Office action for a list 	s have been received. s have been received in a rity documents have bee u (PCT Rule 17.2(a)).	Application No n received in this National Stage
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	Paper No	Summary (PTO-413) (s)/Mail Date Informal Patent Application (PTO-152)

DETAILED ACTION

Information Disclosure Statement

1. The information disclosure statement filed 01/29/04 fails to comply with 37 CFR 1.98(a)(1), which requires the following: (1) a list of all patents, publications, applications, or other information submitted for consideration by the Office; (2) U.S. patents and U.S. patent application publications listed in a section separately from citations of other documents; (3) the application number of the application in which the information disclosure statement is being submitted on each page of the list; (4) a column that provides a blank space next to each document to be considered, for the examiner's initials; and (5) a heading that clearly indicates that the list is an information disclosure statement. The information disclosure statement has been placed in the application file, but the information referred to therein has not been considered.

Drawings

2. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the voltage downconverter, voltage conversion circuits, conversion-bypass control circuits, plurality of signals as input, semiconductor substrate, necessary voltage conversion and control circuits, switching device transistors fabricated in semiconductor process employed to fabricate the common semiconductor substrate, switching regulator, linear regulator, signal communication devices wireless RF, optical and non-contact signal communication method through a common package substrate, filter capacitor, electroApplication/Control Number: 10/766,270

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chemical, electro-thermal, electro-mechanical and electro-optic devices must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filling date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

⁽b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

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4. Claims 1-20 are rejected under 35 U.S.C. 102(b) as being anticipated by Hutson (3,184,653).

Hutson disclose a switching circuit as claimed in Figs. 8-13. Fig. 8 discloses a first switch (100), bypass switch or second switch (104), inductor (58), first switch and inductor are in series, first switch (100) connected to the input power node at joint of (103, 53 and 107) and bypass switch or second switch (104) also connected to the input power node and the second end of the bypass switch or second switch is connected to ioint (71) and the inductor other end is also connected to the same joint (71).

Claim Rejections - 35 USC § 103

- 5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 6. Claims 1-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over ta Huang et al. (4,191,986) in view of Muramatsu et al. (2005/0062434).

Ta Huang et al. teach the claimed invention except that a diode instead of a transistor as a switching device. Muramatsu et al. show that a transistor is an equivalent device know in the art. Ta Huang et al. teach a power line transient suppressors in Fig. 2. Input node is the joint point of the two diodes (24 and 25) and both the diode are connected the two inductors (21 and 22) which is connected to the output terminal (which is connected to the load). Therefore, because these two devices

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were art-recognized equivalent at the time the invention was made, one of ordinary skill in the art would have found it obvious to substitute a diode for a transistor.

Conclusion

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Adolf Berhane whose telephone number is 571-272-2077. The examiner can normally be reached on Monday- Friday 8 AM to 6 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Karl Easthom can be reached on 571-272-1989. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Adolf Berhane Primary Examiner Art Unit 2838

Baldry